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Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a) Derive the Boolean expressions fo* Design and Implementation of Digital Circuits using MOSFETS (Problems and Solutions Included) ... Problem on Complex

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Anant Agarwal and Jeffrey H. Lang Department of Electrical Engineering and Computer Science Massachusetts Institute of Technology
c 1998 Anant Agarwal and Jeffrey H. Lang July 3, 2005

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The vocabulary of digital electronics talks about these two voltages as 'logic 0' and 'logic 1'. Somewhere in a particular design, these will be defined, usually as a range of possible voltages. For example, logic 0 may be defined as any value between 0V and 1.0V, while logic 1

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