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Design for

Manufacturing(DFM)

|GUIDELINES|

ENGINEERING STUDY

MATERIALS From

Prototype to Market:

Design for

Manufacturability

with Untapped

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for Manufacturability  
(DFM) and Design for  
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/u0026 Jay

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1: What is Design for  
Manufacture and  
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Manufacturing DFMA  
Design of Experiment  
(DOE): Introduction,  
Terms and Concepts  
with Practical

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Impact Your Future -  
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reader through all the  
aspects of  
manufacturability and  
yield in a nano-CMOS  
process and how to  
address each aspect  
at the proper design  
step starting with the  
design and layout of  
standard cells and  
how to yield-grade  
libraries for critical

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model based  
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mask planning,  
simulation and  
manufacturing, and  
through statistical  
design ...

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CMOS walks the  
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aspects of  
manufacturability and  
yield in a nano-CMOS  
process and how to  
address each aspect  
at the proper design  
step starting with the



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through statistical

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...

The design for  
manufacturability  
(DFM)/yield objective  
can then be  
expressed in the  
following way (curve  
2 in Fig. 1): (1)

Provide better

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Processes  
transferring the  
process from the R&D  
line to the  
manufacturing line.

(2) Speed-up the  
learning curve  
climbing.

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Abstract This tutorial focuses on the dcsim strategies for VLSI circuits that are aimed at achieving manufacturable, high-yielding, chips. We review the cUrrrent status of statistical design methodologies modeling and based ...

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### Design for

Manufacturability and  
Yield The existence of  
this layered structure  
has allowed  
organizations to  
concentrate on areas  
where they can add  
the most value,  
whereas the  
introduction of

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Nano Scale  
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standard approaches  
and hand-offs at layer  
boundaries have  
allowed for increasing  
levels of abstraction  
with the  
understanding that  
the underlying  
components are  
sound.

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y And Yield For  
manufacturability  
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(also sometimes  
known as design for  
manufacturing or  
DFM) is the general  
engineering practice  
of designing products  
in such a way that  
they are easy to  
manufacture. The  
concept exists in  
almost all engineering

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disciplines, but the  
implementation  
differs widely  
depending on the  
manufacturing  
technology.

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(DFM) and Design for  
Testability (DFT)

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the best  
differentiators to  
identify a leading  
electronic contract  
manufacturer (CM).  
DFM and DFT reports  
detect and address  
issues before they  
cost you money on  
the production line.

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about the various  
aspects of  
manufacturability and  
yield in a nano cmos  
process and how to  
address each aspect

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## Manufacturability And Yield For

Design For

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The concepts of

Design for

Manufacturability and

Design for Yield

DFM/DFY are

bringing together

domains that co-

existed mostly



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Separated until now  
– circuit design,  
physical design and  
manufacturing  
process. New  
requirements like  
SoC, mixed  
analog/digital design  
and deep-submicron  
technologies force to  
a mutual integration  
of all levels.

**DFM/DFY Design for**  
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DFMA Advantages  
Quantitative method  
to assess design  
Communication tool  
with other  
engineering  
disciplines and other  
departments (Sales,  
etc.) Greater role for  
other groups while  
still in the  
“ engineering ”

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Phase such as  
Manufacturing Since  
almost 75% of the  
product cost is  
determined in the  
“ engineering ”  
phase, it gives a tool  
to attack

Overview of Design  
for Manufacturing  
and Assembly (DFMA)  
Design for Excellence  
or Design For

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Excellence, are terms  
and expansions used  
interchangeably in  
the existing literature,  
where the X in design  
for X is a variable  
which can have one  
of many possible  
values. In many fields  
X may represent  
several traits or  
features including:  
manufacturability,  
power, variability,

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cost, yield, or  
reliability. This gives  
rise to the terms  
design for  
manufacturability,  
design for inspection,  
design for variability,  
design for cost.  
Similarly, other  
disciplines may

Design for X -  
Wikipedia

This complete suite of  
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Manufacturability and  
variability solutions is  
used by both  
designers and  
manufacturers to  
improve design  
manufacturability and  
reduce the time to  
yield. Cadence DFM  
solutions comprise:  
Litho Physical  
Analyzer (LPA)  
Improves your  
systematic and

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### parametric yield and meets foundry DFM signoff requirements.

## Nano Scale

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Overview  
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Manufacturing and  
Assembly  
Terminology: Design  
for... (DFx) <sup>3</sup>/<sub>4</sub>Design  
for Manufacturing  
(DFM) refers to

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Ones

Design activity that is based on minimizing the cost of production and/or time to market for a product, while maintaining an appropriate level of quality. A primary strategy in DFM involves minimizing the number of parts in a product.



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Design For  
Manufacturability and  
Concurrent  
Engineering are  
proven design  
methodologies that  
work for any size  
company. The  
process often can cut  
in half costs and time-  
to-market while

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### Manufacturability

Adding significant improvements to quality and delivery.

### And Field For

### Nano Scale

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NORMAN NOBLE, INC  
Design for Assembly  
Principles Minimize  
part count Design  
parts with self-  
locating features  
Design parts with self-  
fastening features

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reorientation of parts  
during assembly

Design parts for  
retrieval, handling, &  
insertion Emphasize

‘ Top-Down ’

assemblies

Standardize

parts...minimum use  
of fasteners.

Encourage modular  
design

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## PDF Design For

### Manufacturability

#### Design for

##### Manufacturing &

###### Assembly

Dramatically reduce back-and-forth with your manufacturing partner and avoid costly re-spins and field failures by validating design manufacturability at design-time instead of post-design.

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Providing all the tools  
in a single  
environment  
eliminates the time-  
consuming need for  
data to be translated  
between departments,  
which often results in  
errors and  
intelligence gaps.

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With SOLIDWORKS®  
Design-to-  
Manufacturing  
Solution, concept to  
final assembly work  
can now happen  
concurrently, in one  
seamlessly integrated  
and managed system.

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